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## PATENT APPLICATION

FHE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Eiichi TAKAHASHI et al.

Group Art Unit: 2116

Application No.: 10/668,357

Examiner:

E. Chang

Filed: September 24, 2003

Docket No.:

105635.01

For:

TIMING ADJUSTMENT OF CLOCK SIGNALS IN A DIGITAL CIRCUIT

## REQUEST FOR RECONSIDERATION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In reply to the March 29, 2005 Office Action, reconsideration of the rejection is respectfully requested in light of the following remarks.

Claims 1-36 are pending.

The Office Action rejects claims 1-36 under judicially created doctrine of obviousness-type double patenting over U.S. Patent No. 6,658,581 to Takahashi et al. (Takahashi) in view of U.S. Patent No. 6,480,832 to Nakisa. Attached hereto is a Terminal Disclaimer. Thus, the rejection is moot. Accordingly, withdrawal of the rejection is respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims 1-36 are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfullysu

fames A. Oliff

Registration No. 27,075

Robert A. Miller

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JAO:RAM/tls

Attachment:

Terminal Disclaimer

Date: May 24, 2005

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